

APR 15 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Edwin A. Suominen Art Unit: : 2687
Serial No. : 10/649,305 Examiner: : Bhattacharya, S.
Filing Date : August 25, 2003
Title : Simplified High Frequency Tuner and Tuning Method

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant discloses the reference listed on the attached form PTO-1449.

Please feel free to telephone the undersigned if it would in any way advance prosecution of this application.

Respectfully submitted,

Dated: April 15, 2005



Edwin A. Suominen
Reg. No. 43,174

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Form PTO-1449 (Modified)

Serial No.: 10/649,305

**List of Patents and
Publications for Applicant's
Information Disclosure
Statement**

Applicant(s): Edwin A. Suominen

Group: 2681

Filed: August 25, 2003

Page 1 of 1

OTHER ART

Examiner

Initials

Author, Title, Date, Pertinent Pages, Etc.

A

Steyaert, M. and Crols J. "Analog Integrated Polyphase Filters," in Workshop on Advances in Analog Circuit Design (Eindhoven, Netherlands), March 29-31, 1994. Cited copy is from R.J. van de Plassche *et al.* (eds.), Analog Circuit Design, pp. 149-166, Copyright 1995, Kluwer Academic Publishers.

Examiner: _____

Date Considered: _____

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance *and* not considered. Include copy of this form with next communication to applicants.

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Client Number: KIM EMMONS MAR 21 2005
Author: MICHEL STEYAERT, JAN CROLS
Date: 1995
Article Title: ANALOG INTEGRATED POLYPHASE FILTERS
Report Number: CALL NUMBER MAIN TK7874 A534 1994
Series Title: WORKSHOP OF ADVANCES IN ANALOGUE CIRCUIT DESIGN (3RD: 1994: EINDHOVEN,
NETHERLANDS) ANALOG CIRCUIT DESIGN: LOW-POWER LOW-VOLTAGE, INTEGRATED
FILTERS AND POWER/EDITED BY RUDY J. VAN DE PLASSCHE, WILLY M.C. SANSEN AND
JOHAN H. HUIJSING
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